



TFT MODULE SPECIFICATION

RVT43HLTNWCA0 V2.0A

IPS RGB 4.3" display datasheet
Rev. 1.0
2026-06-05

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ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally black/IPS	/
Size	4.3	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	105.50 x 67.20 x 5.00	mm
Active Area (W x H)	95.04 x 53.86	mm
Pixel Pitch (W x H)	0.198 x 0.198	mm
Resolution	480 x 272 (RGB)	/
Brightness	800	cd/m ²
LCD Interface Type	RGB	/
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
LCD Driver	SC7283	/
With/Without Touch	With Capacitive Touch Panel	/
CTP IC	ILI2132A	/
LCD Input Voltage	3.3	V
Bonding Technology	Air Bonding	/
Weight	TBD	g

Note 1. RoHS3 compliant

Note 2. LCM weight tolerance: ± 5%.

1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2026-06-05	Initial release	

2. CONTENTS

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3. MODULE CLASSIFICATION INFORMATION

RV	T	43	H	L	T	N	W	C	A0	V2.0A
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.	11.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	43 – 4.3”
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	L – 480 x 272 px
6.	INTERFACE	T – TFT LCD, RGB
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel
10.	VERSION	A0 – aTouch
11.	MODULE REVISION	V2.0A

5. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT
Analog supply voltage	V_{CI}	-0.3	+4.0	V
Backlight Forward Current	I_F	-	40	mA
Operating Temperature	T_{OP}	-20	70	°C
Storage Temperature	T_{ST}	-30	80	°C

Note. The above are maximum values. If exceeded, they may cause permanent damage to the unit.

6. ELECTRICAL CHARACTERISTICS

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Digital supply voltage		V_{DDI}	3.0	3.3	3.6	V	-
Input Signal Voltage	Low Level	V_{IL}	0	-	$0.3V_{DD}$	V	RXIN0- ,RXIN0+,RXI N1- ,RXIN1+,RXI N2- ,RXIN2+SELB ,RESET,STBY B,L/R,U/D
	High Level	V_{IH}	$0.7V_{DD}$	-	V_{DD}	V	
Touch Panel Supply Current		I_{CTP}	-	99	-	mA	Measured average current, touch panel active

7. TOUCH PANEL CHARACTERISTICS

PARAMETER	SYMBOL	VALUE	REMARK
Touch panel interface	-	I ² C	
I ² C 7-bit slave address	ADDR _{7-bit}	0x41	Standard I ² C address format
I ² C write address	ADDR _W	0x82	8-bit address format
I ² C read address	ADDR _R	0x83	8-bit address format

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Backlight Driving Voltage	V _F	21.6	24.0	26.4	V	Notes 1, 2
Backlight Driving Current	I _F	-	40	-	mA	
Backlight Power Consumption	W _{BL}	-	960	-	mW	
Backlight Lifetime	-	-	50,000	-	hours	Note 3

Note 1. Unless specified, the ambient temperature T_a = 25°C

Note 2. The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range be exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.

Note 3. If LED is driven by high current, the lifetime of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating lifetime is estimated data.

9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C	-	30	40	ms	FIG 1.	4, 7
Contrast Ratio	Cr		640	800	-	---	FIG 2.	1, 7
Surface Luminance	Lv		-	800	-	cd/m ²		2, 7
Uniformity	U	-	75	80	-	%	FIG 2.	3
Viewing Angle Range	θ	$\phi = 90^\circ$	70	80	-	deg	FIG 3.	6
		$\phi = 270^\circ$	70	80	-	deg		
		$\phi = 0^\circ$	70	80	-	deg		
		$\phi = 180^\circ$	70	80	-	deg		
CIE (x, y) Chromaticity	Rx	$\theta=0^\circ$ $\phi=0^\circ$ Ta=25 °C	-0.03	0.590	+0.03	-	FIG 2.	5,7
	Ry			0.350		-		
	Gx			0.379		-		
	Gy			0.558		-		
	Bx			0.146		-		
	By			0.137		-		
	Wx			0.325		-		
	Wy			0.354		-		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 2.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 2.

$$Lv = \text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}$$

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of 5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 2.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, T_r) and from black to white (Decay Time, T_f). For additional information see Figure 1. The test equipment is BM-7A.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then make average value.

Note 6. For TFT module, viewing angle is the angle at which the contrast ratio is greater 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 3.

Note 7. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

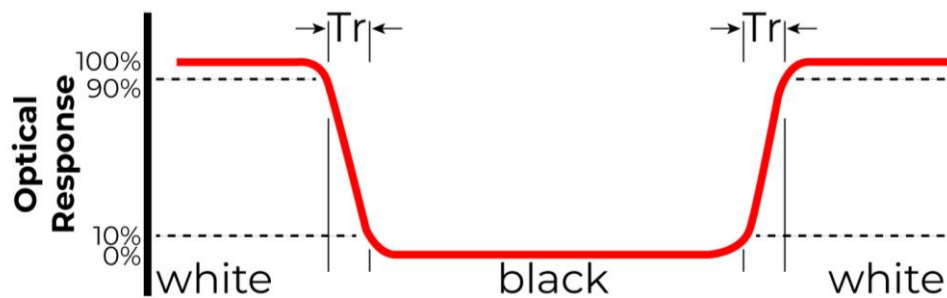


Figure 1. The definition of response time

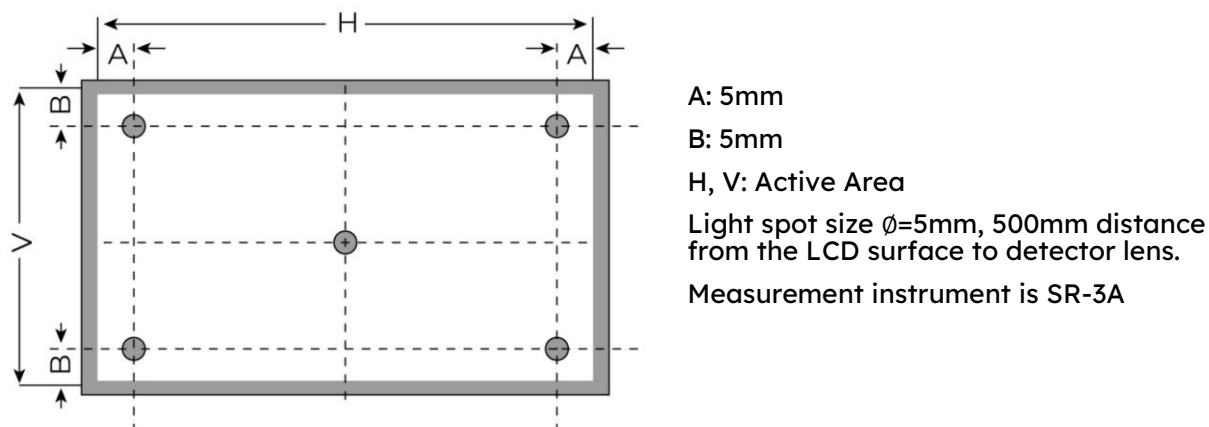


Figure 2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity

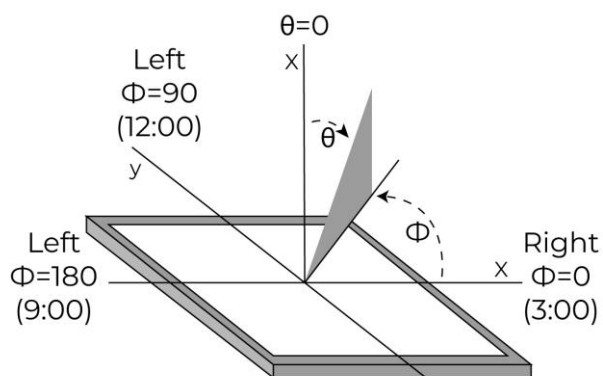


Figure 3. The definition of viewing angle

10. INTERFACES DESCRIPTION

10.1 TFT ASSIGNMENT

PIN	SYMBOL	DESCRIPTION
1	LEDK	Power supply for LED
2	LEDA	Power supply for LED
3	GND	Ground
4	VDD	Power Supply Voltage
5-12	DR0-DR7	Red Data
13-20	DG0-DG7	Green Data
21-28	DB0-DB7	Blue Data
29	GND	Ground
30	PCLK	Pixel Clock signal in RGB interface
31	DISP	Display on/off Control
32	HSYNC	Horizontal sync signal in RGB interface
33	VSNC	Vertical sync signal in RGB interface
34	DE	Data Enable signal in RGB interface
35	NC	No connection
36	GND	Ground
37	NC	No Connection
38	NC	No Connection
39	NC	No Connection
40	NC	No Connection

10.2 Touch Panel Connector

PIN	SYMBOL	DESCRIPTION
1	USB_GND	USB Ground
2	USB_VDD	USB Power for CTP, 5.0V
3	USB_D-	USB Data signal-
4	USB_D+	USB Data signal+
5	I2C_GND	I2C Ground
6	I2C_VDD	I2C Power for CTP 3.3V
7	I2C_RST	I2C Reset pin, active low
8	I2C_SCL	I2C Clock input
9	I2C_INT	I2C Interrupt signal from CTP, active low
10	I2C_SDA	I2C Data signal

10.3 CN1 assignment

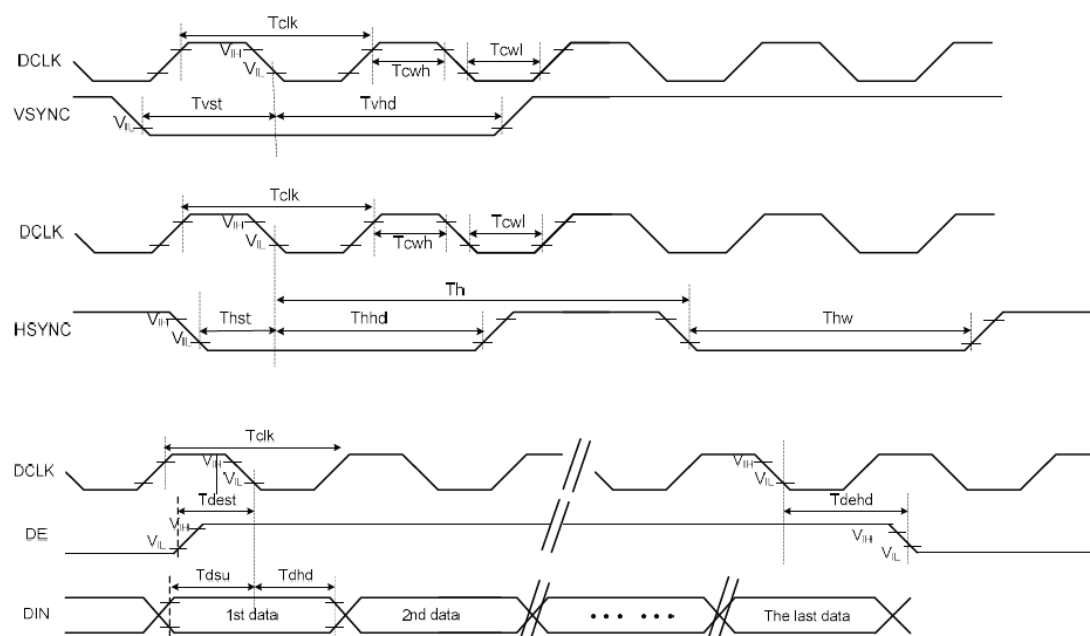
PIN	SYMBOL	DESCRIPTION
1	USB_VDD	USB_Power for CTP, DC 5.0V
2	USB_D-	USB_Data signal -
3	USB_D+	USB_Data signal +
4	USB_GND	USB_Ground

11. TIMING CHART

11.1 Timing characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	CONDITIONS
CLK Pulse Duty	Tclk	40	50	60	%	
HSYNC Width	Thw	2	-	-	DLCK	
HSYNC Period	Th	55	60	65	us	
VSYNC Setup Time	Tvst	10	-	-	ns	
VSYNC Hold Time	Tvhd	10	-	-	ns	
HSYNC Setup Time	Thst	10	-	-	ns	
HSYNC Hold Time	Thhd	10	-	-	ns	
Data Setup Time	Tdsu	10	-	-	ns	
Data Hold Time	Tdhd	10	-	-	ns	
DE Setup Time	Tdest	10	-	-	ns	
DE Hold Time	Tdehd	10	-	-	ns	

11.2 System bus timing for RGB interface

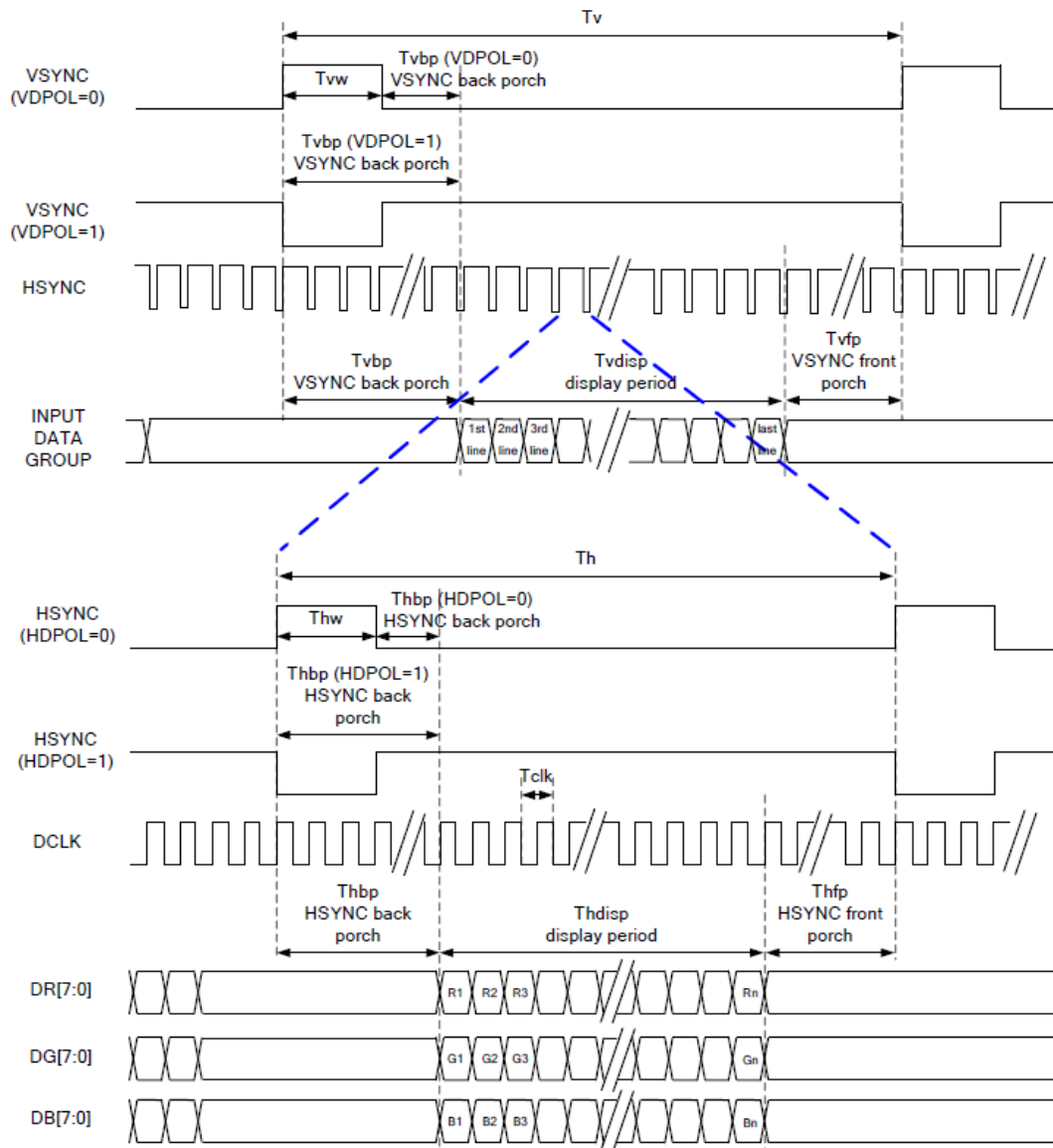


11.3 DC Electrical Characteristics

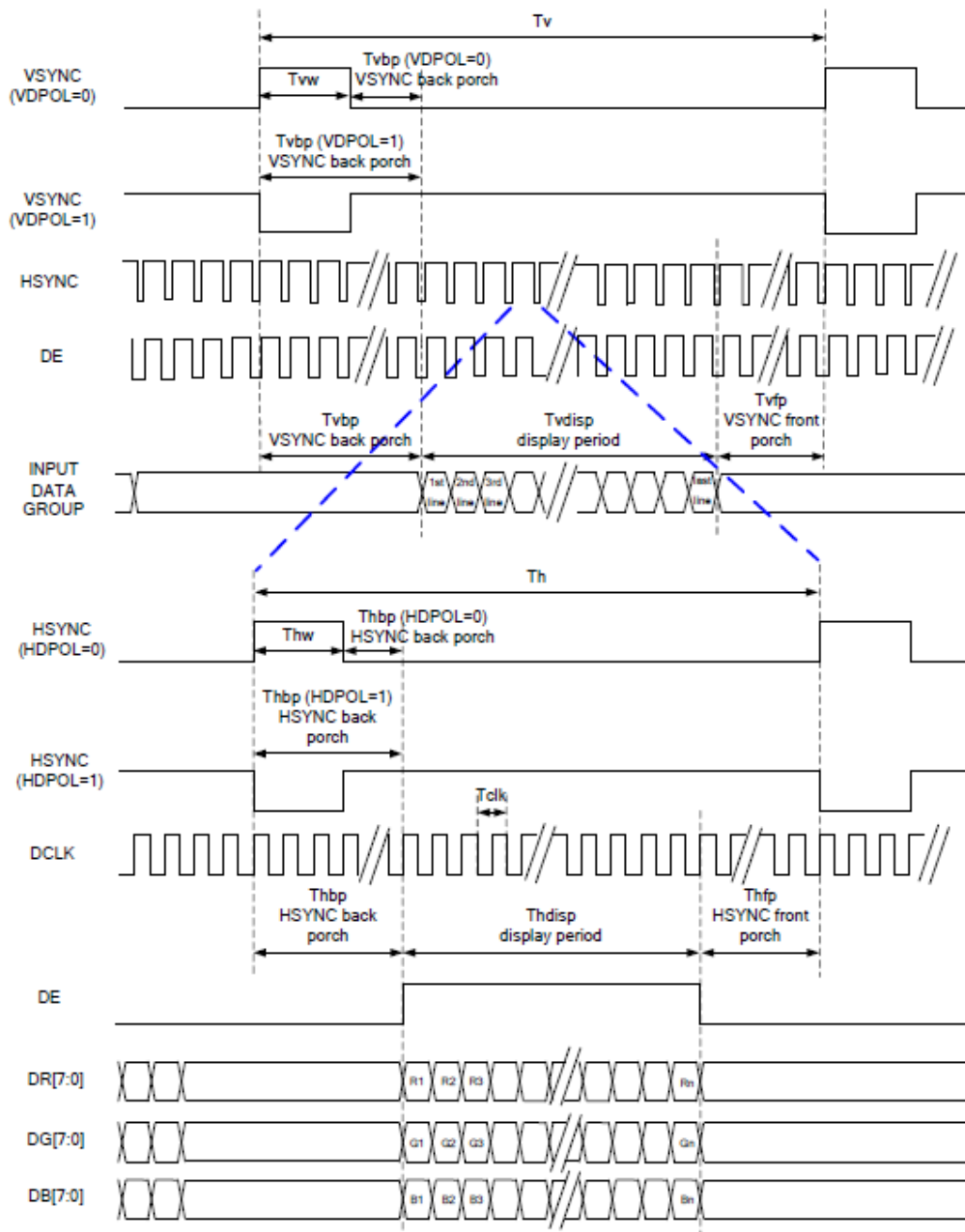
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
Analog Supply Voltage	VCI	3.0	3.3	3.6	V	-
Input High Voltage	VIH	0.7VDDI	-	VDDI	V	Digital input pins
Input Low Voltage	VIL	DGND	-	0.3VDDI	V	Digital input pins
Output High Voltage	VOH	VDDI-0.4	-	VDDI	V	Digital input pins
Output Low Voltage	VOL	DGND	-	DGND+0.4	V	Digital input pins
Power consumption (Panel+LSI)	Black Mode	-	20	-	mA	VCI=3.3V
Power consumption (Panel+LSI)	Sleeping Mode	-	-	0.005	mA	VCI=3.3V

11.4 RGB Interface

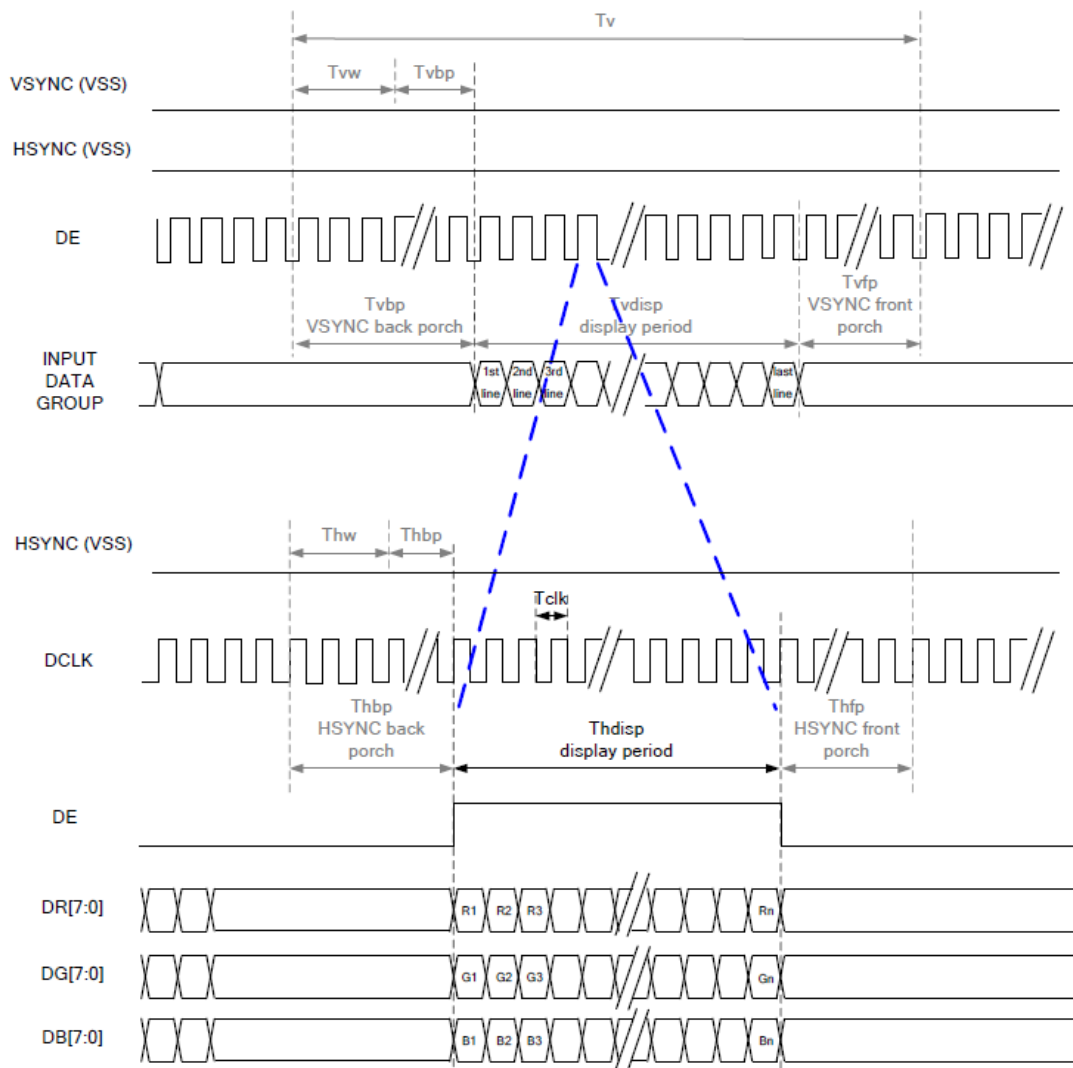
11.4.1 SYNC Mode



11.4.2 SYNC-DE Mode



11.4.3 DE Mode



11.5 Parallel RGB Input Timing

11.5.1 24-bit RGB Input Timing Table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
DCLK Frequency	Fclk	8	9	12	MHz	
DCLK Period	Tclk	83	111	125	ns	
HSYNC Period Time	Th	485	531	598	DCLK	
HSYNC Display Period	Thdisp	480			DCLK	
HSYNC Back Porch	Thbp	3	43	43	DCLK	By H_blanking setting
HSYNC Front Porch	Thfp	2	8	75	DCLK	
HSYNC Pulse Width	Thw	2	4	43	DCLK	
VSYNC Period Time	Tv	276	292	321	HSYNC	
VSYNC Display Period	Tvdisp	272			HSYNC	
VSYNC Back Porch	Tvbp	2	12	12	HSYNC	By V_blanking setting
VSYNC Front Porch	Tvfp	2	8	37	HSYNC	
VSYNC Pulse Width	Tvw	2	4	12	HSYNC	

11.5.2 8-bit RGB Input Timing Table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	REMARK
DCLK Frequency	Fclk	24	27	30	MHz	
DCLK Period	Tclk	33	37	42	ns	
HSYNC Period Time	Th	1445	1491	1558	DCLK	
HSYNC Display Period	Thdisp	1440			DCLK	

HSYNC Back Porch	Thbp	3	43	43	DCLK	By H_blanking setting
HSYNC Front Porch	Thfp	2	8	75	DCLK	
HSYNC Pulse Width	Thw	2	4	43	DCLK	
VSYNC Period Time	Tv	276	292	321	HSYNC	
VSYNC Display Period	Tvdisp	272			HSYNC	
VSYNC Back Porch	Tvbp	2	12	12	HSYNC	By V_blanking setting
VSYNC Front Porch	Tvfp	2	8	37	HSYNC	
VSYNC Pulse Width	Tvw	2	4	12	HSYNC	

12. INSPECTION

Standard acceptance/rejection criteria for TFT module according to document available [here](#).

13. RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	NOTE
1	High Temperature Storage	80°C/120 hours	A,B,C,D,E
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	+60°C , 90%RH, 120hrs	
6	Thermal Cycling Test (No operation)	-20°C /30 min ~ +70°C /30 min for a total 5 cycles, Start with cold temperature and end with high temperature.	A,B,C,D,E
7	Vibration Test	Sweep:10Hz~55Hz~10Hz 2G 2 hours for each direction of X. Y. Z. (6 hours for total)	A,B,C,D,E
8	Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	A,B,C,D,E
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	A,B,C,D,E
9	ESD	Contact=+/-4KV, Air=+/-8KV,(R=330R,C=150pF), 1 sec,9point,10times/point;	A,B,C,D,E

Notes

- A. LCM each function is OK.
- B. LCM appearance inspection without abnormalities (Including scratch, damage, corrosion and serious deformation)
- C. LCM brightness above the Min. value of Spec.
- D. Luminance uniformity above the Min. value of Spec.
- E. Color chromaticity within tolerance range.

14. LEGAL INFORMATION

CE marking is usually obligatory only for a complete end product. Riverdi display modules are semi-finished goods which are used as inputs to become part of the finished products.

Therefore, Riverdi display modules are not CE marked.

This is not a standalone product. It was designed as an electronic component. It needs integration with a whole system to be fully functional.

Riverdi grants the guarantee for the proper operation of the goods for a period of 12 months from the date of possession of the goods. If in a consequence of this guaranteed execution the customer has received the defects-free item as replacement for the defective item, the effectiveness period of this guarantee shall start anew from the moment the customer receives the defects-free item.

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We're here to ensure your project's success, every step of the way!